



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor: Choi, Byung-Jin
App. No.: 10/616,294 GPAU: 1722
Filing Date: July 9, 2003 Examiner: Thukhanh T. Nguyen
Dkt. No.: P92-34-03 Conf. No.: 1597
For: Systems for Magnification and Distortion Correction for
Imprint Lithography Processes

INFORMATION DISCLOSURE STATEMENT

Mail Stop AMENDMENT
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

The following information is submitted in compliance with Applicants' duty of disclosure and pursuant to 37 C.F.R. § 1.56, § 1.97 and § 1.98. The undersigned brings the patents, publications, applications or other information identified in the attached:

- ☒ Form(s) PTO/SB/08A and/or PTO/SB/08B or PTO/1449
☐ Other:

to the Examiner's attention in the above-identified application. It is respectfully requested that the cited information be expressly considered during the prosecution of this application, and the references be made of record therein and appear among the "references cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed:

before the mailing date of a first Office action on the merits or before the mailing date of a first Office action after the filing of a request for continued examination under § 1.114. Therefore, no fee is believed required.

Applicants do not believe that any additional fees are due, but if the Commissioner believes additional fees are due, the

Commissioner is hereby authorized to charge any fees which may be required, or credit any overpayment, to Deposit Account Number 502650.

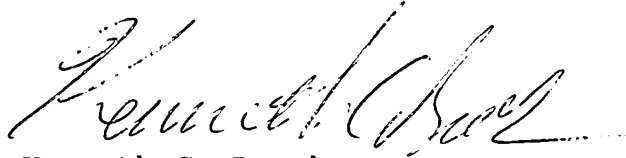
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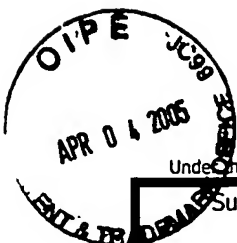
Signed: Katrina Prati
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Respectfully Submitted,



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Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT***(use as many sheets as necessary)*

Sheet 1 of 8

Complete if Known

APPLICATION NUMBER	10/616,294
FILING DATE	07/09/2003
FIRST NAMED INVENTOR	Choi et al.
Group Art Unit	1722
Examiner Name	Nguyen, Thukhanh T.
Attorney Docket Number	P92-34-03

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (if known)			
	C1	5,414,514		Smith et al.	5/9/1995	
	C2	5,808,742		Everett et al.	9/15/1998	
	C3	6,088,103		Everett et al.	7/11/2000	
	C4	6,137,562		Masuyuki et al.	10/24/2000	
	C5	6,334,960	B1	Willson et al.	1/1/2002	
	C6	6,391,217	B2	Schaffer et al.	5/21/2002	
	C7	6,522,411	B1	Moon et al.	2/18/2003	
	C8	6,696,220	B2	Bailey et al.	2/24/2004	
	C9	2004/0007799	A1	Choi et al.	1/15/2004	
	C10	2004/0021254	A1	Sreenivasan et al.	2/5/2004	
	C11	2004/0021866	A1	Watts et al.	2/5/2004	
	C12	2004/0022888	A1	Sreenivasan et al.	2/5/2004	
	C13	2004/0112861	A1	Choi et al.	6/17/2004	
	C14	2003/0092261	A1	Kondo et al.	5/15/2003	
	C15	2003/0179354	A1	Araki et al.	9/25/2003	
	C16	2002/0177319	A1	Chou	11/28/2002	
	C17	2002/0150398	A1	Choi et al.	10/17/2002	
	C18	6,713,238	B1	Chou et al.	3/30/2004	

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Sheet 2 of 8		APPLICATION NUMBER 10/616,294		FILING DATE 07/09/2003	
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	C19	2004/0008334	A1	Sreenivasan et al.	1/15/2004	
	C20	2004/0009673	A1	Sreenivasan et al.	1/15/2004	
	C21	2004/0124566	A1	Sreenivasan et al.	7/1/2004	
	C22	2004/0089979	A1	Rubin	5/13/2004	
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	C26	6,150,231		Muller et al.	11/21/2000	
	C27	6,489,068	B1	Kye	12/3/2002	
	C28	6,646,662	B1	Nebashi et al.	11/11/2003	
	C29	6,770,852	B1	Stegner	8/3/2004	
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	C31	4,600,309		Fay	7/15/1986	
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	C33	5,074,667		Miyatake	12/24/1991	
	C34	5,148,036		Matsugu et al.	9/15/1992	
	C35	5,148,037		Suda et al.	9/15/1992	
	C36	6,518,189	B1	Chou	2/11/2003	

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	C37	2002/0132482	A1	Chou	9/19/2002	
	C38	2002/0167117	A1	Chou	11/14/2002	
	C39	6,482,742	B1	Chou	11/19/2002	
	C40	2003/0081193		White et al.	5/1/2003	
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	C42	2004/0036201	A1	Chou et al.	2/26/2004	
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	C44	6,828,244	B2	Chou	12/7/2004	
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	C49	2004/0156108	A1	Chou et al.	8/12/2004	
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	C52	2004/0118809	A1	Chou et al.	6/24/2004	
	C53	2004/0046288	A1	Chou	3/11/2004	
	C54	2003/0080472	A1	Chou	5/1/2003	

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	C55	2003/0034329	A1	Chou	2/20/2003	
	C56	2002/0042027	A1	Chou et al.	4/11/2002	
	C57	6,636,311	B1	Ina et al.	10/21/2003	
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	C62	6,630,410	B2	Trapp et al.	10/7/2003	
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	C64	10/999,898		Cherala et al.	11/30/2004	
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	C67	6,295,120	B1	Miyatake	9/25/2001	
	C68	6,049,373		Miyatake	4/11/2000	
	C69	6,285,439	B1	Miyatake	9/4/2001	
	C70	6,791,669	B2	Poon	9/14/2004	
	C71	6,517,995	B1	Jacobenson et al.	2/11/2003	
	C72	4,256,829		Daniel	3/17/1981	

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(use as many sheets as necessary)

Sheet	5	of	8
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		Office ³	Number ⁴	Kind Code ⁵ (if known)				

	C78	WO	00/21689		Chou et al.	4/20/2000		
	C79	JP	02-92603		Kurikawa et al.	4/3/1990		
	C80	JP	02-24848		Kamio	1/26/1990		
	C81	WO	01/47003		Steiner et al.	6/28/2001		
	C82	WO	02/07199		Chou	1/24/2002		
	C83	WO	2004/114016		Chou et al.	12/29/2004		
	C84	WO	99/05724		Chou et al.	2/4/1999		
	C85	WO	03/010289		Chou et al.	2/6/2003		
	C86	WO	03/079416		Chou	9/25/2003		
	C87	WO	03/099536		Chou et al.	12/4/2003		

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Sheet	7	of	8	Attorney Docket Number	P92-34-03

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²

	C88	Colburn et al., Step and Flash Imprint Lithography for sub-100 nm Patterning, Proceedings of SPIE Vol. 3997, pp. 453 - 457, 1/1/2000	
	C89	Chou et al., Imprint Lithography with Sub-10 nm Feature Size and High Throughput, Microelectronic Engineering 35, pp. 237 - 240, 1/1/1997	
	C90	Johnson et al., Advances in Step and Flash Imprint Lithography, SPIE Microlithography Conference, 2/23/2003	
	C91	Heidari, Nanoimprint Lithography at the 6 in. Wafer Scale, J. Vac. Sci. Technol. B 18(6), pp. 3557 - 3560, 11/1/2000	
	C92	White et al., Novel Alignment System for Imprint Lithography, J. Vac. Sci. Technol. B 18(6), pp. 3552 - 3556, 11/1/2000	
	C93	Translation of Japanese Patent 02-92603	
	C94	Translation of Japanese Patent 02-24848	
	C95	Chou et al., Ultrafast and Direct Imprint of Nanostructures in Silicon, Nature, Col. 417, (June 2002), pp. 835-837, 6/1/2002	
	C96	Chou et al., Nanoimprint Lithography, Journal of Vacuum Science Technology B 14(16), pp. 4129-4133, 11/1/1996	

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Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²

	C97	Colburn et al., Development and Advantages of Step-and-Flash Lithography, Solid State Technology, 7/1/2001	
	C98	Colburn et al., Characterization and Modeling of Volumetric and Mechanical Properties for Step and Flash Imprint Lithography Photopolymers, Journal of Vacuum Science Technology. Vol b. 19(6), 11/1/2001	
	C99	Bailey et al., Step and Flash Imprint Lithography: Defect Analysis, Journal of Vacuum Science, B 19(6), pp. 2806-2810, 11/1/2001	
	C100	Bailey et al., Step and Flash Imprint Lithography: Template Surface Treatment and Defect Analysis, Journal of Vacuum Science, B 18(6), pp. 3572-3577, 11/1/2000	
	C101	Wronosky et al., Wafer and Reticle Positioning System for the Extreme Ultraviolet Lithography Engineering Test Stand, Emerging Lithography Technologies, Proceedings of SPIE Vol. 3997, pp. 829-839, 7/1/2000	
	C102	Schneider et al., Stripes of Partially Fluorinated Alkyl Chains: Dipolar Langmuir Monolayers	
	C103	Martin et al., Predication of Fabrication Distortions in Step and Flash Imprint Lithography Templates, Journal of Vacuum Science Technology B 20(6), pp. 2891-2895, 11/1/2002	

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